

FIG. 4

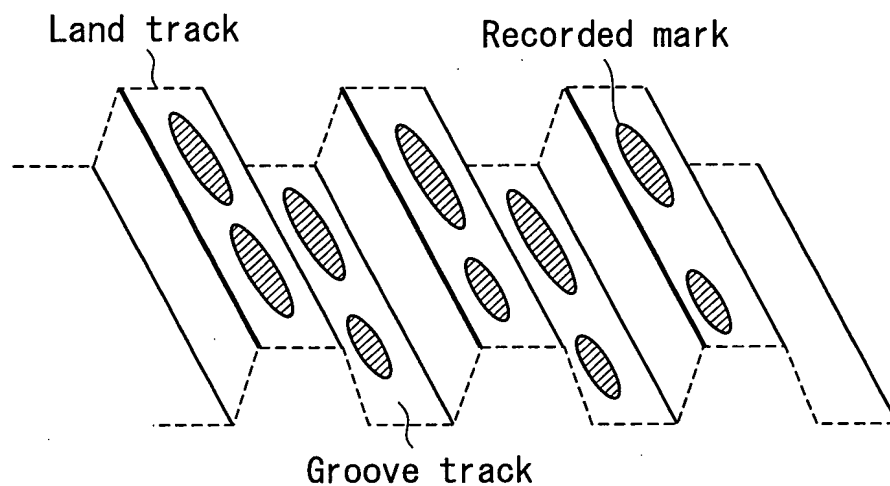


FIG. 5A

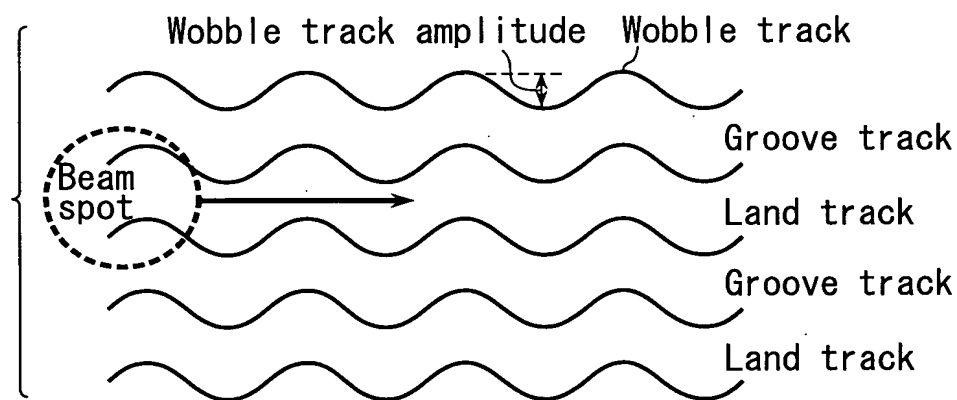


FIG. 5B

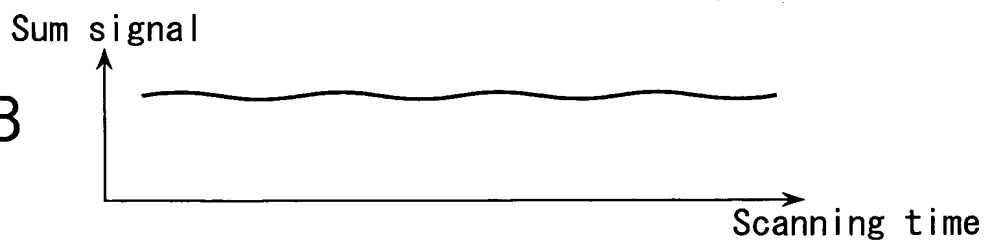
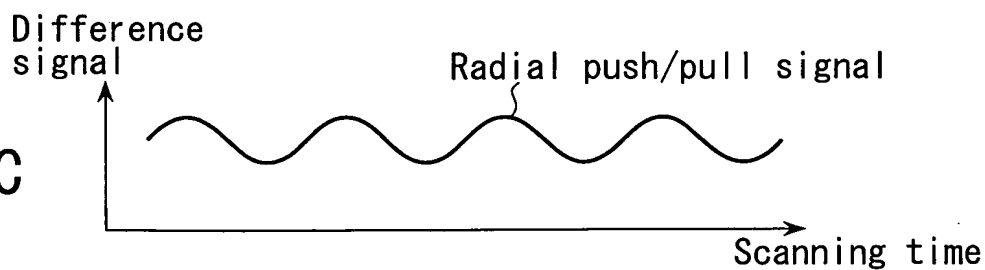


FIG. 5C



Frequency modulation

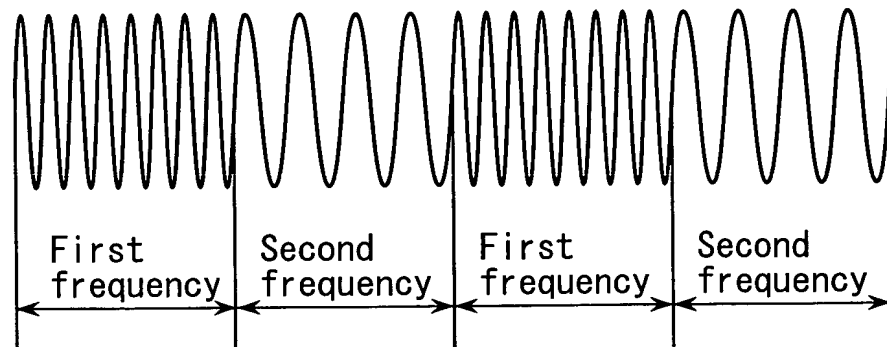


FIG. 6A

Phase modulation

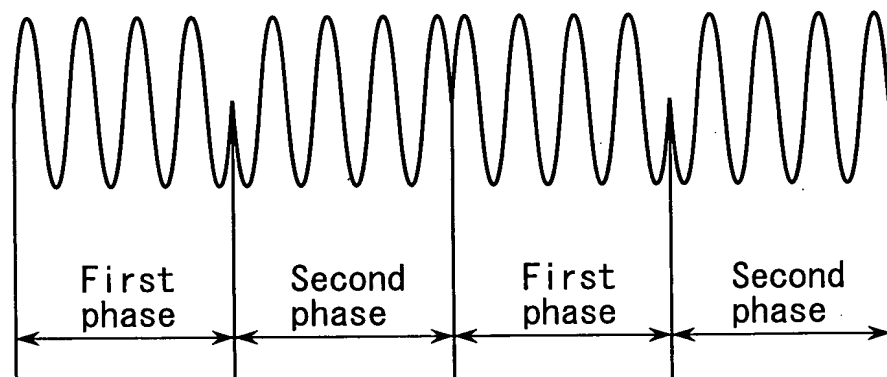


FIG. 6B

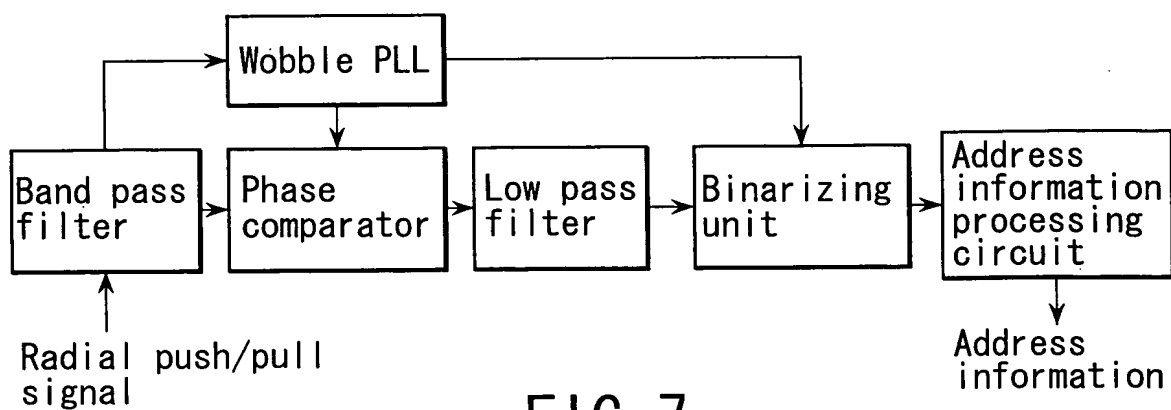


FIG. 7

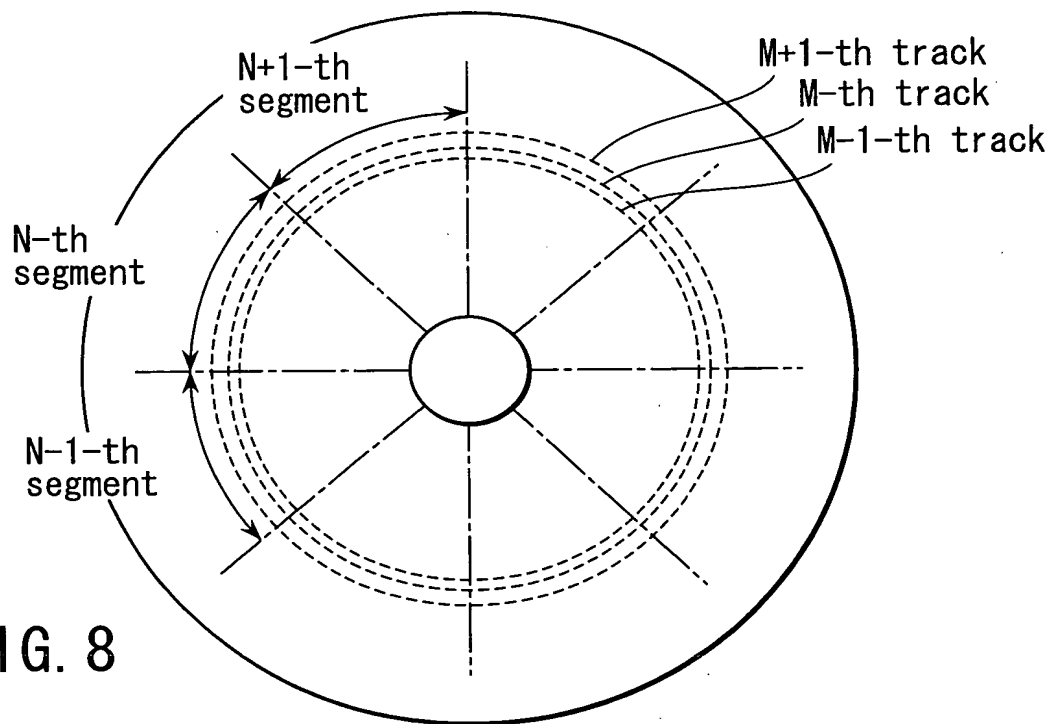
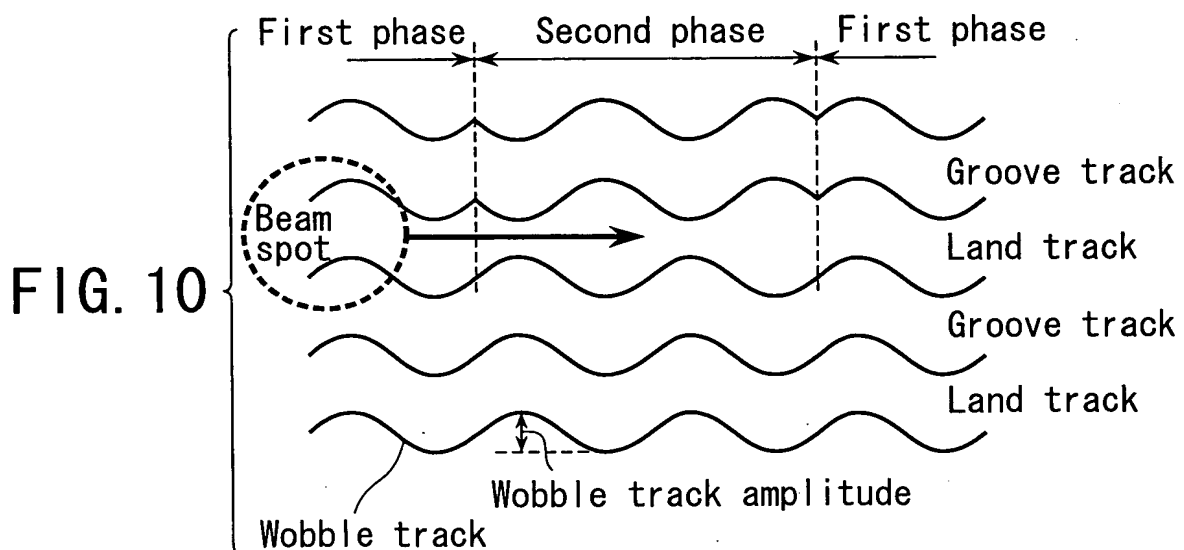
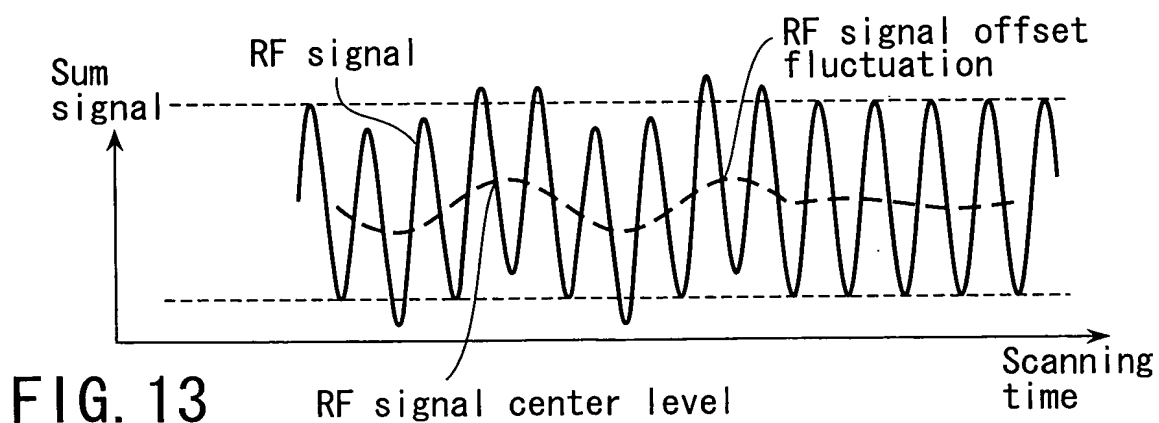
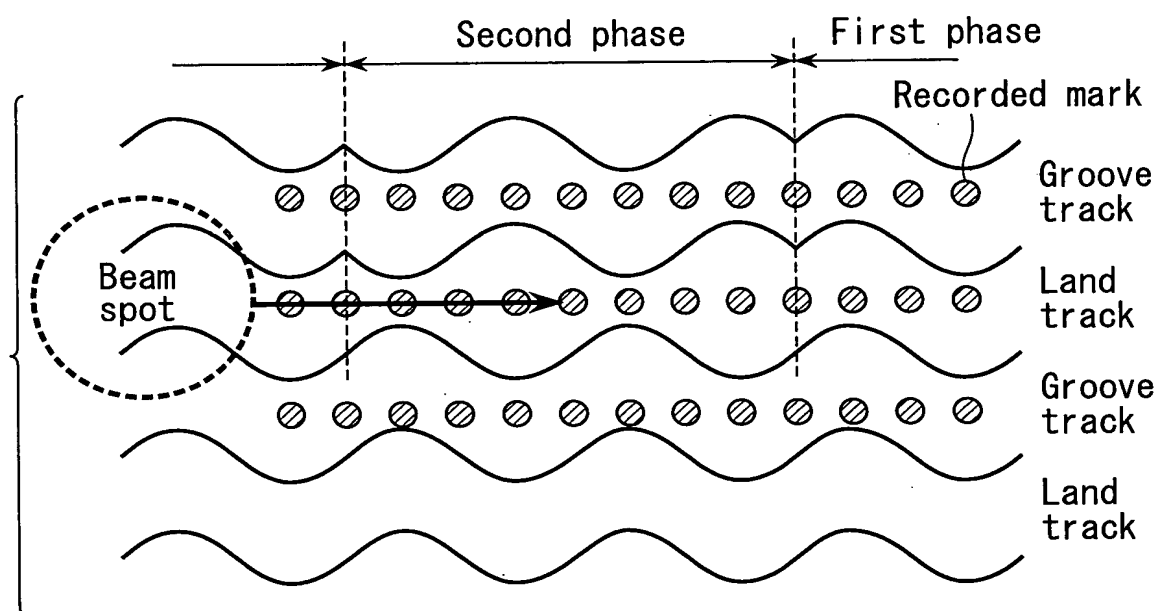
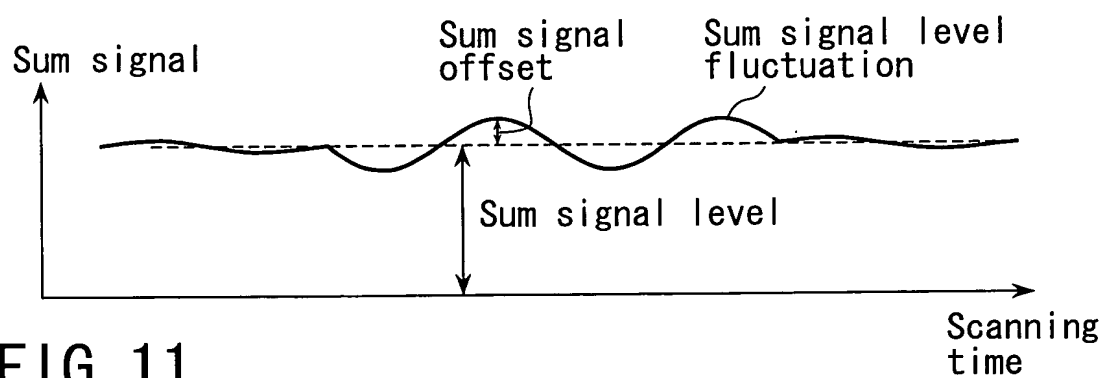


FIG. 8

Synchronous signal	Segment number	Track number	Correction bit	Other information
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FIG. 9





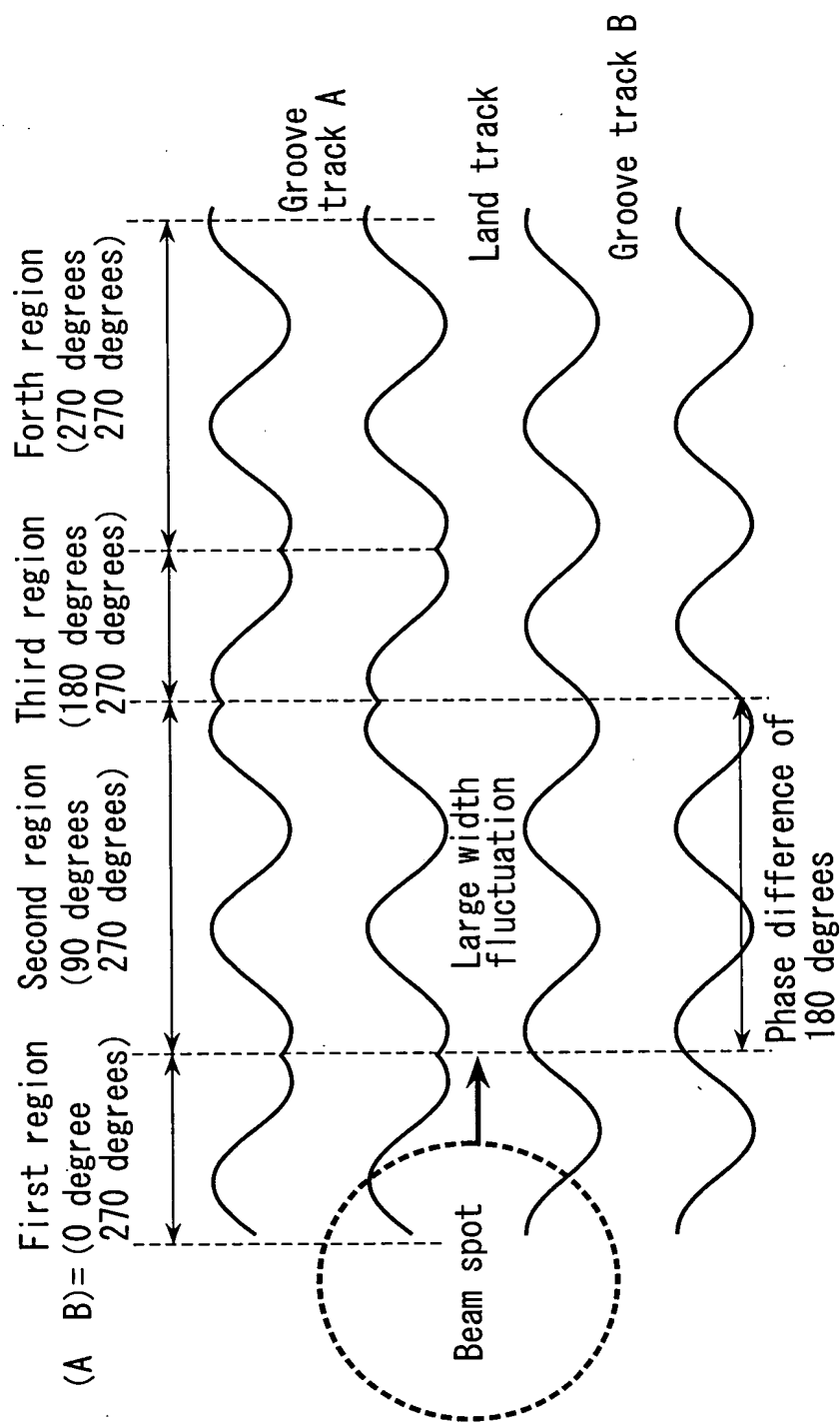


FIG. 14

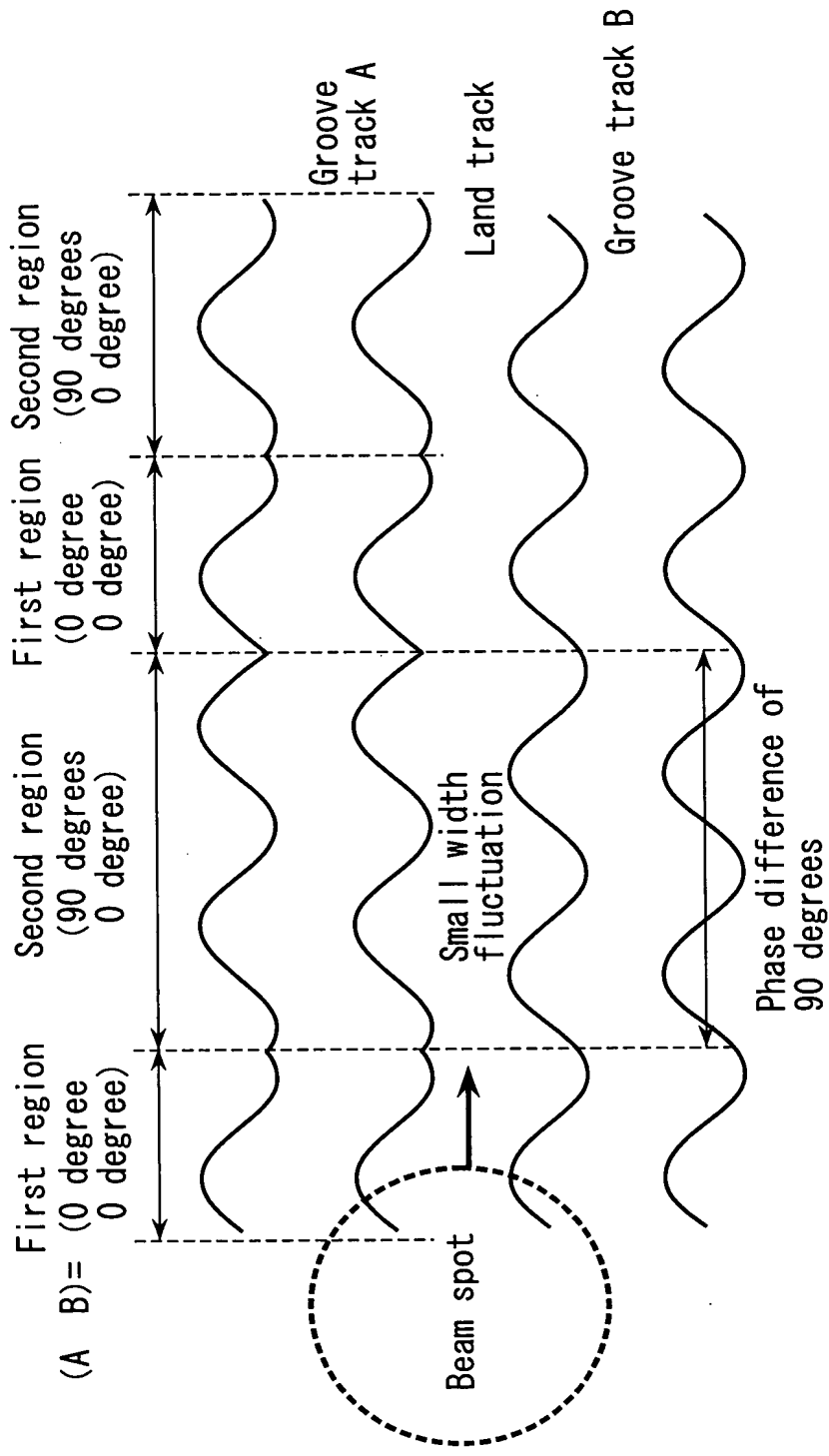


FIG. 15

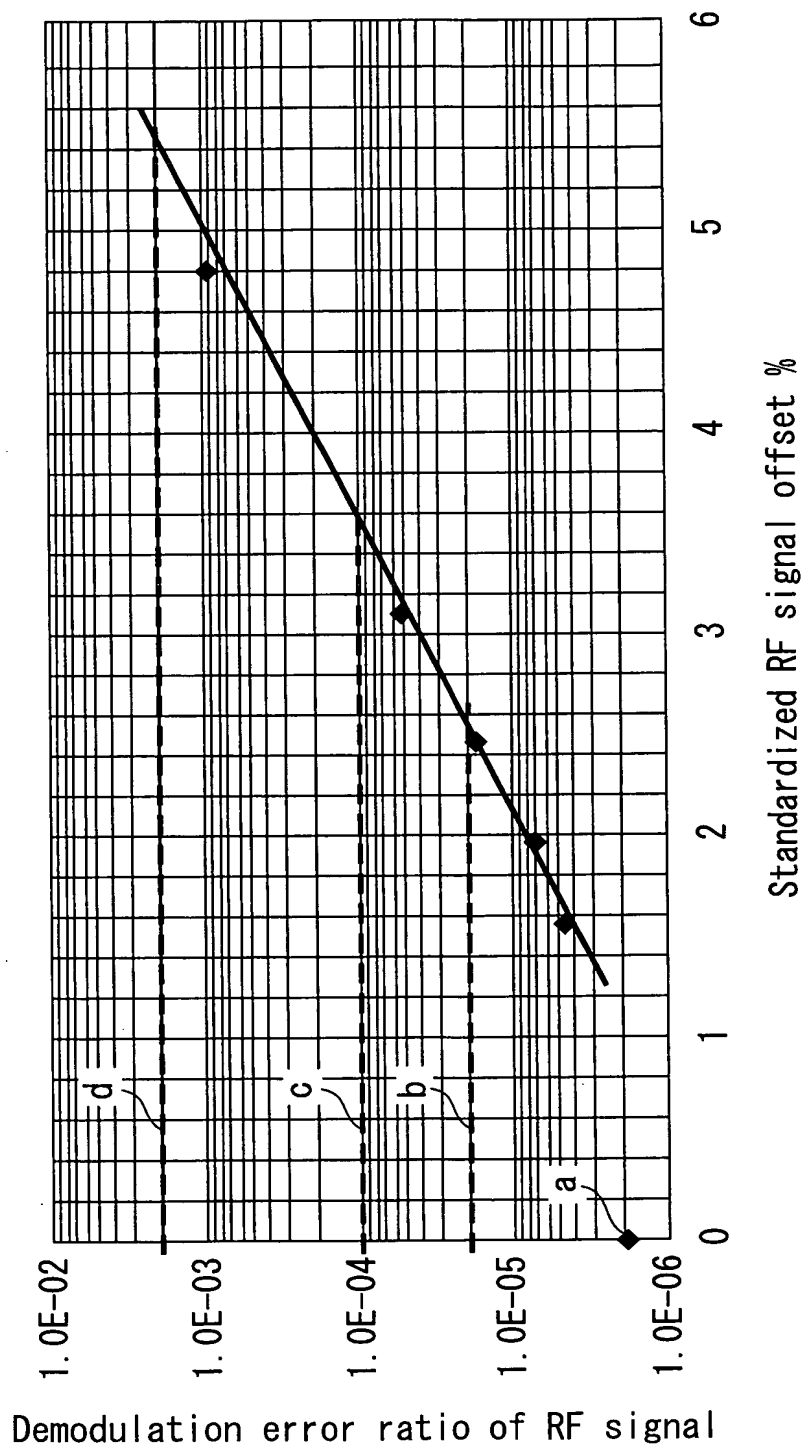


FIG. 16



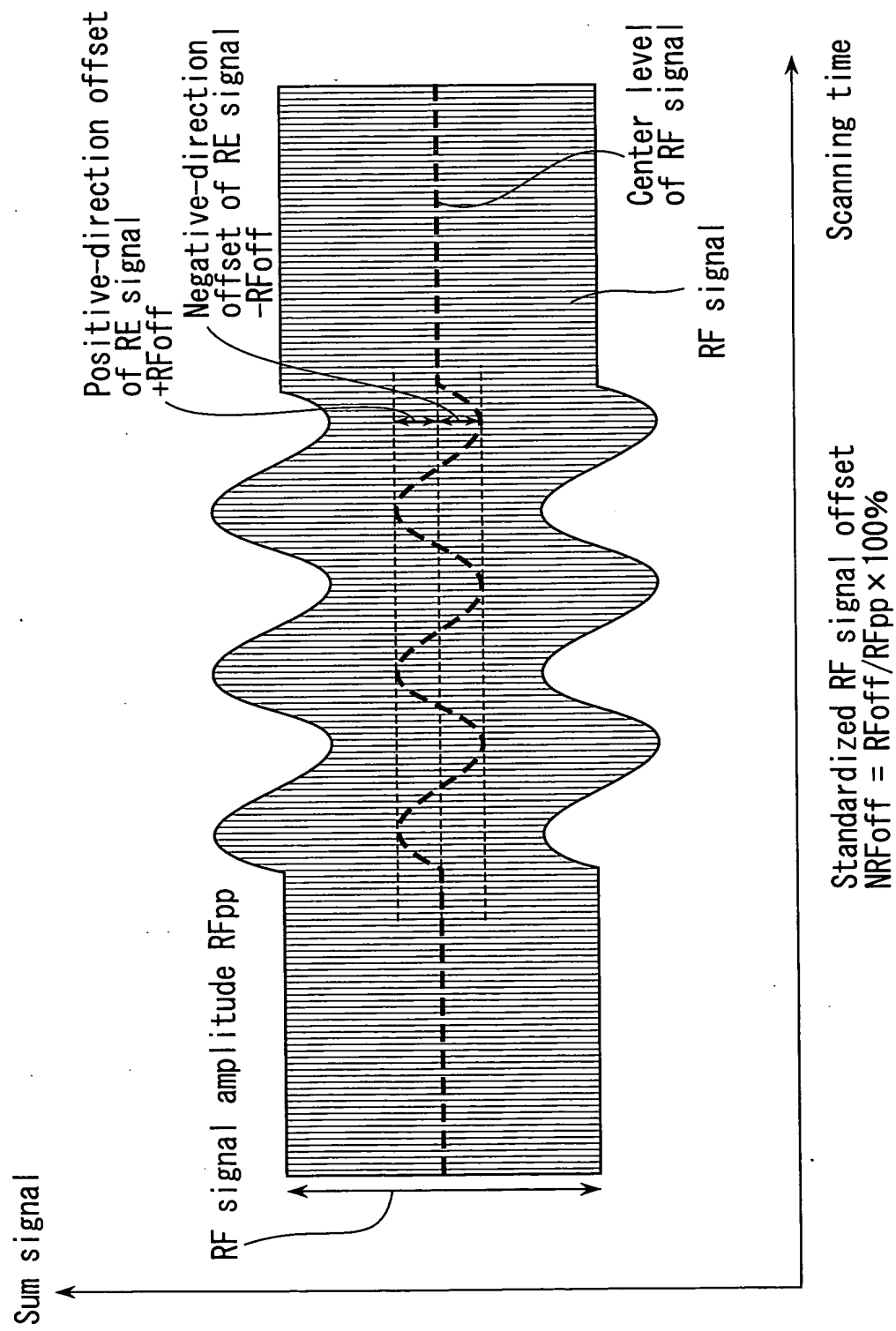


FIG. 17

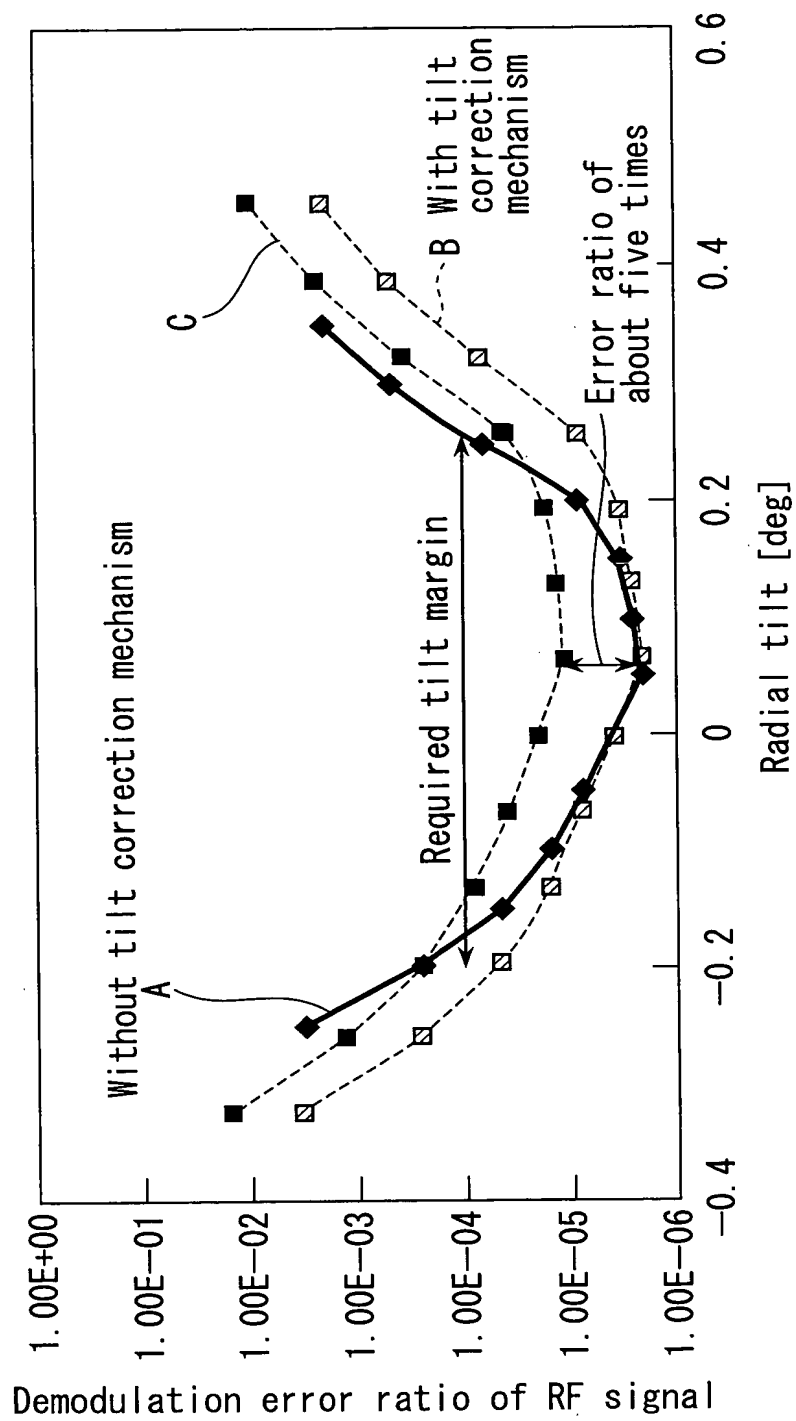


FIG. 18

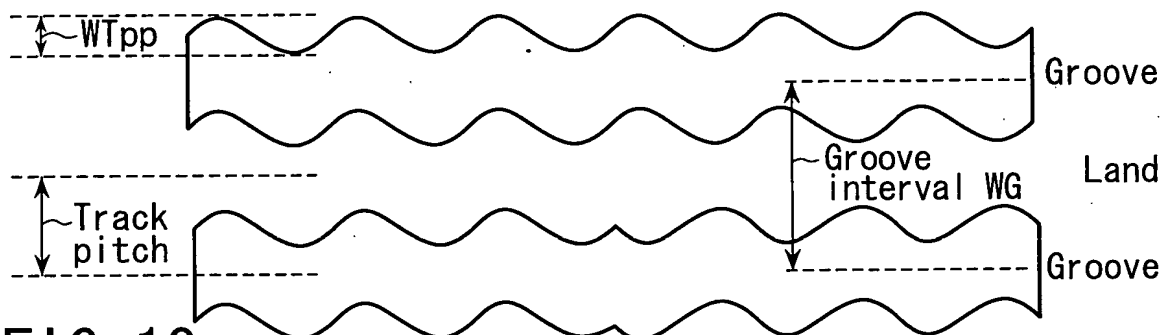


FIG. 19

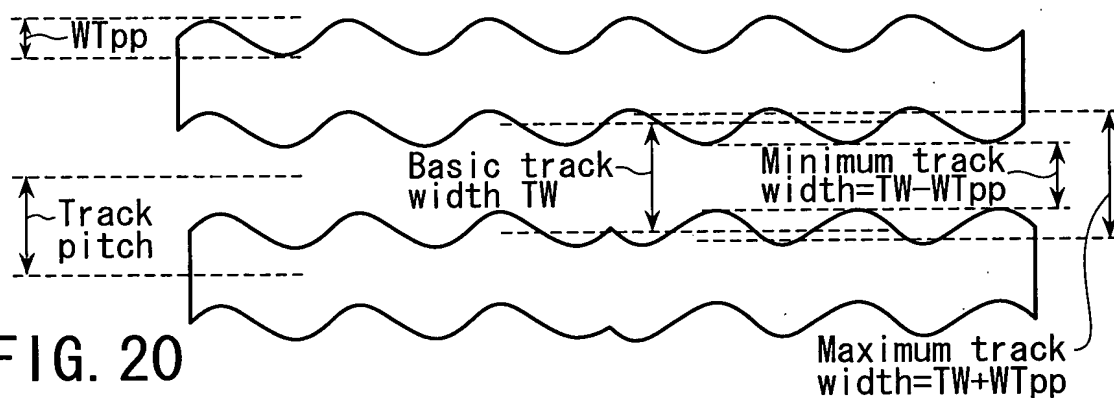


FIG. 20

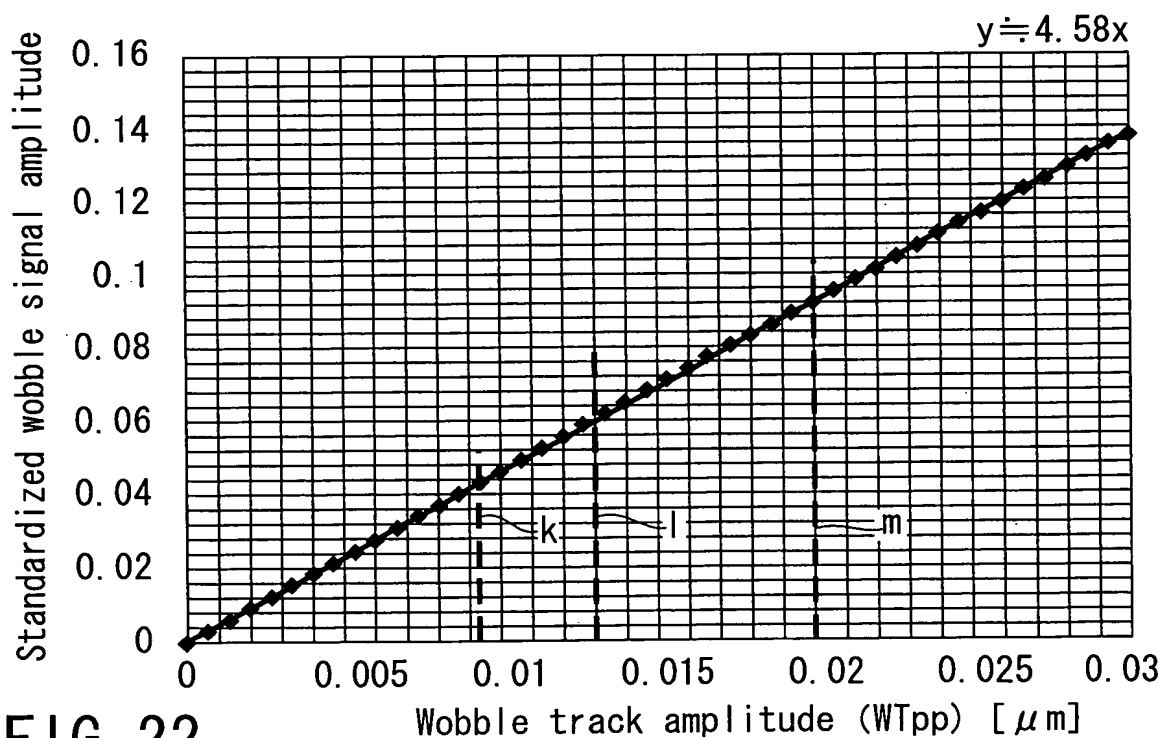


FIG. 22

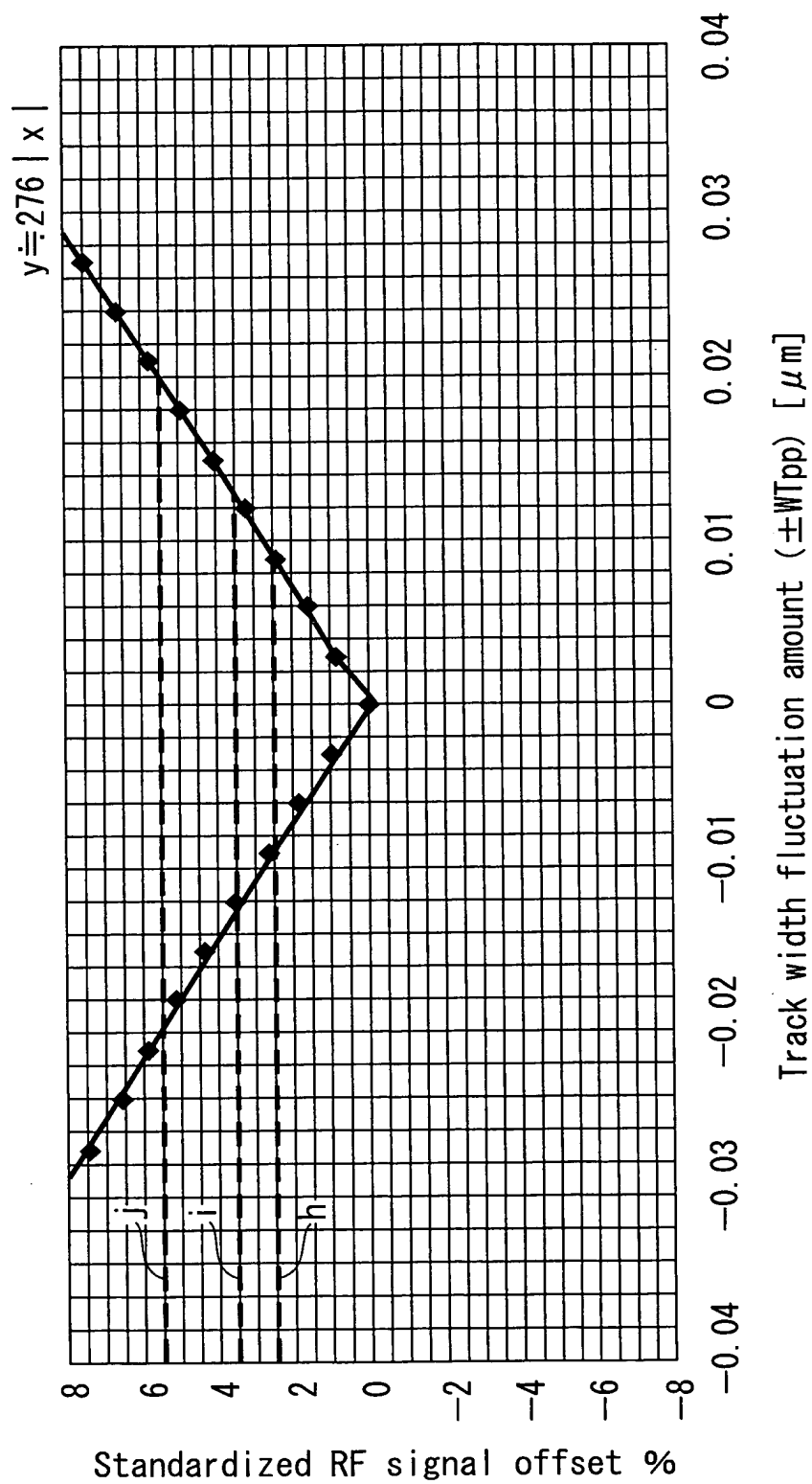


FIG. 21

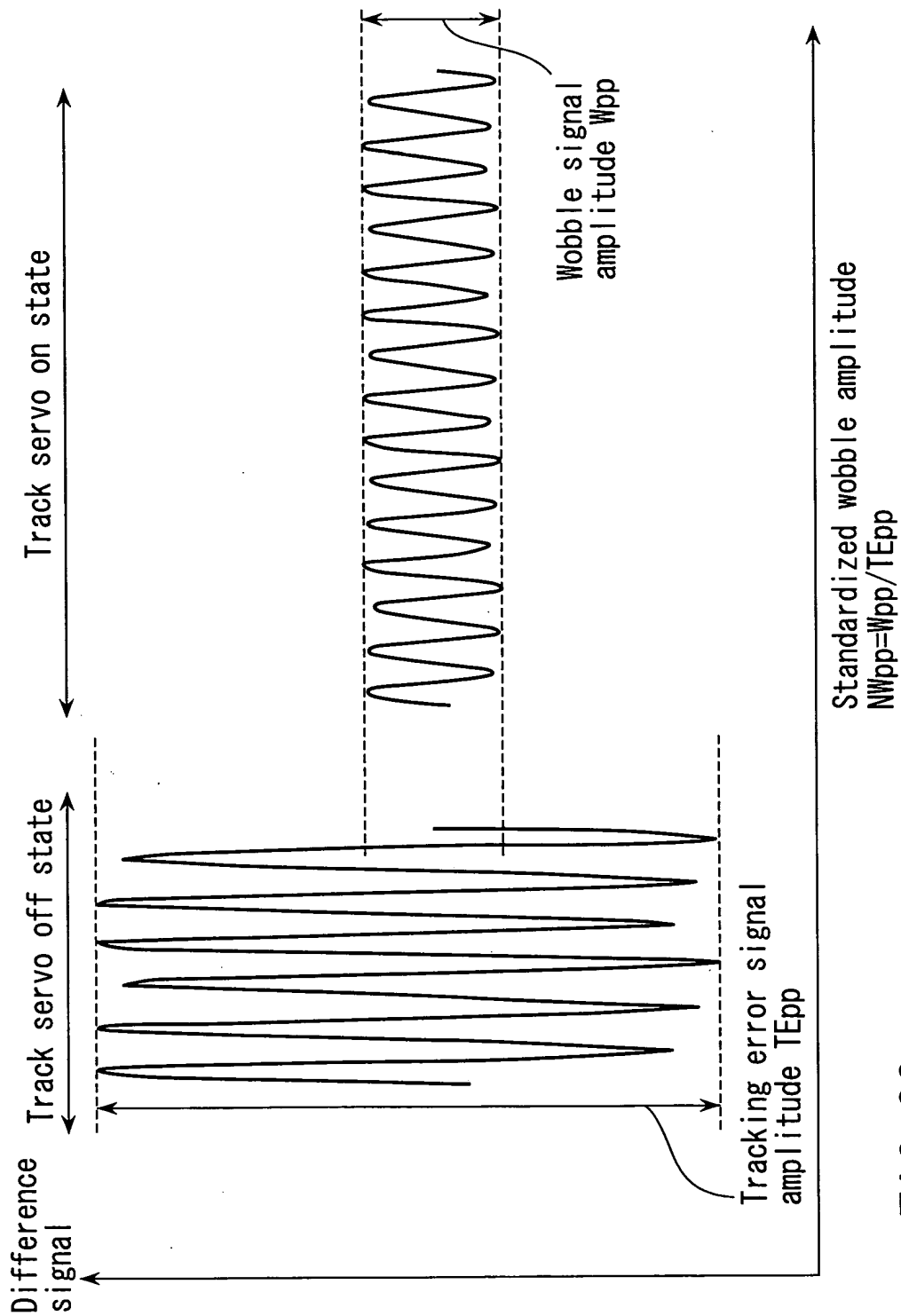


FIG. 23

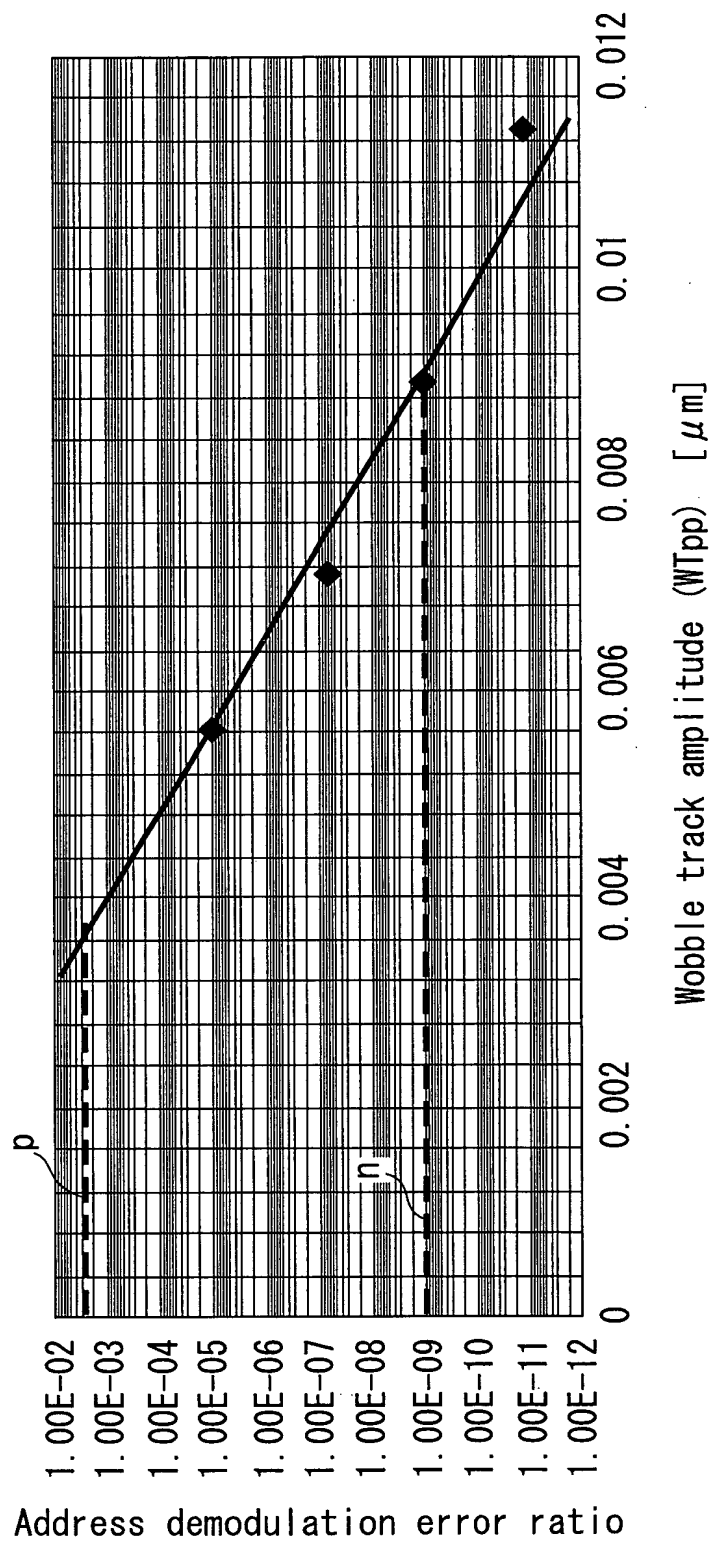


FIG. 24

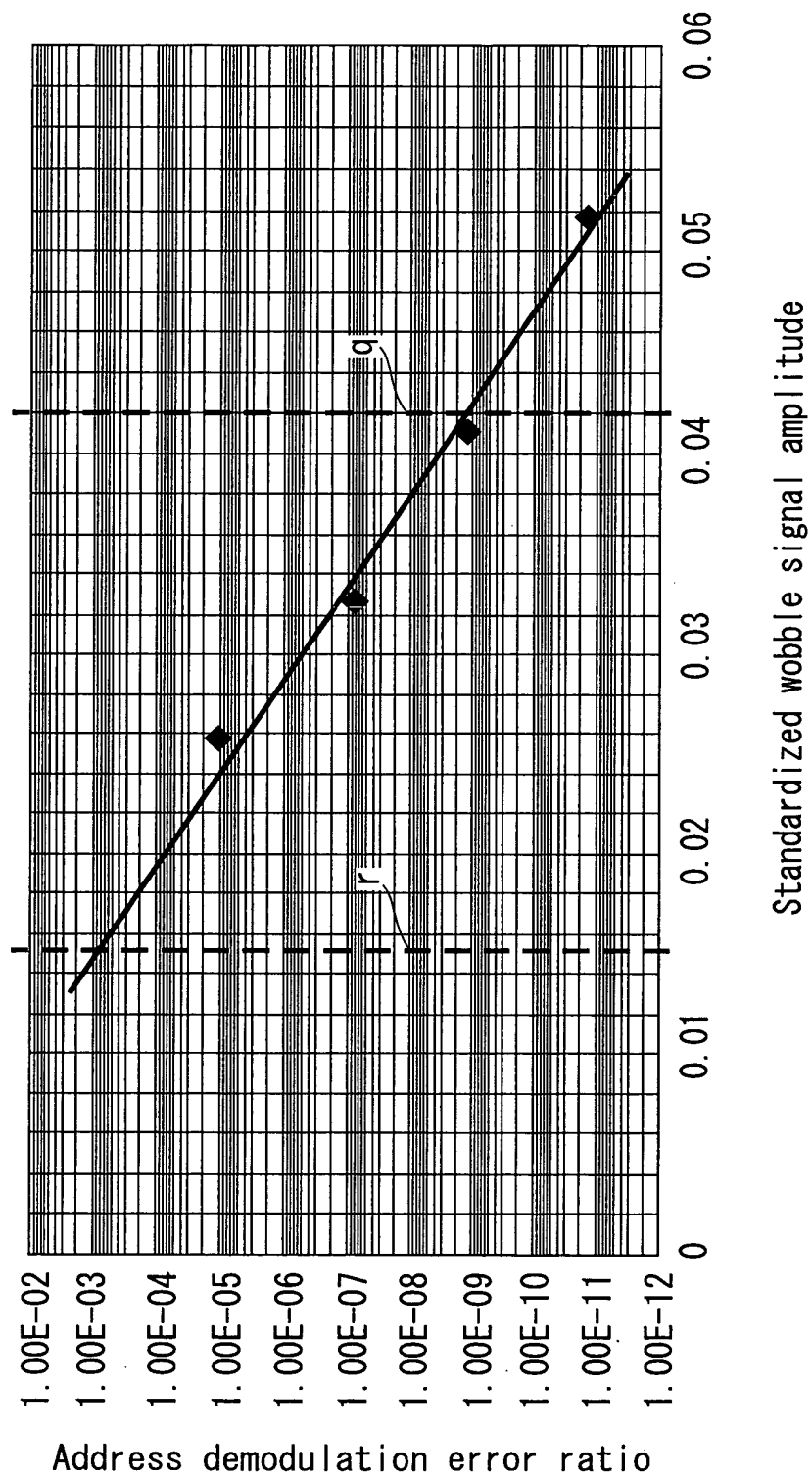


FIG. 25

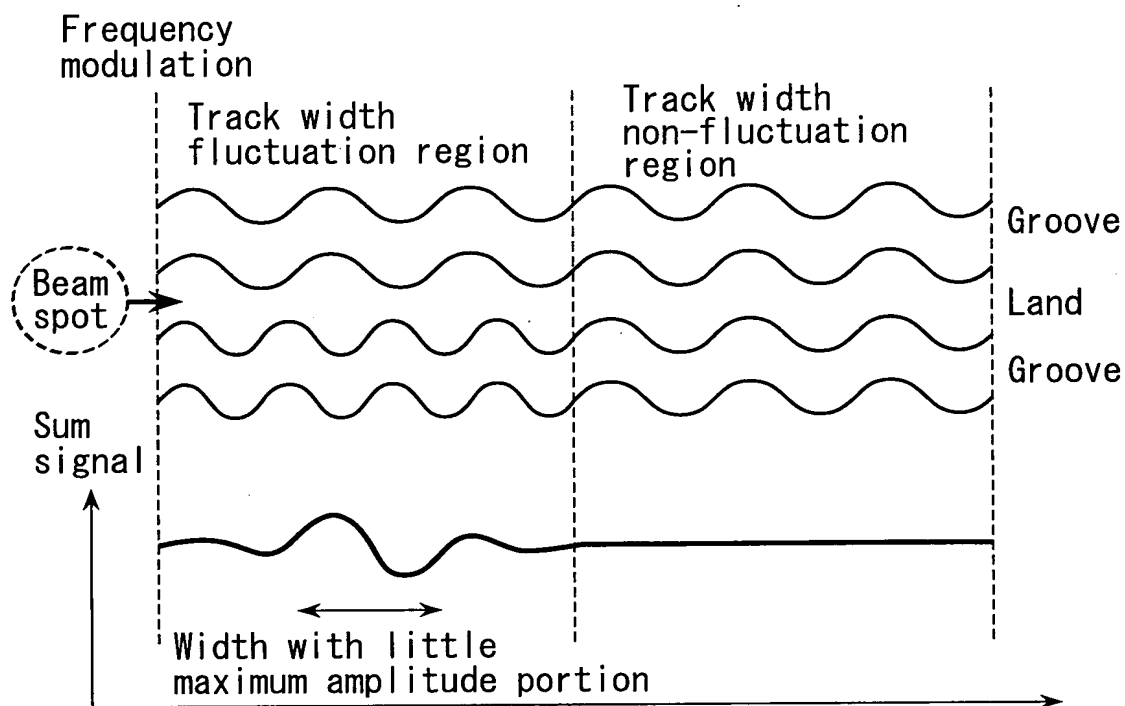


FIG. 26A

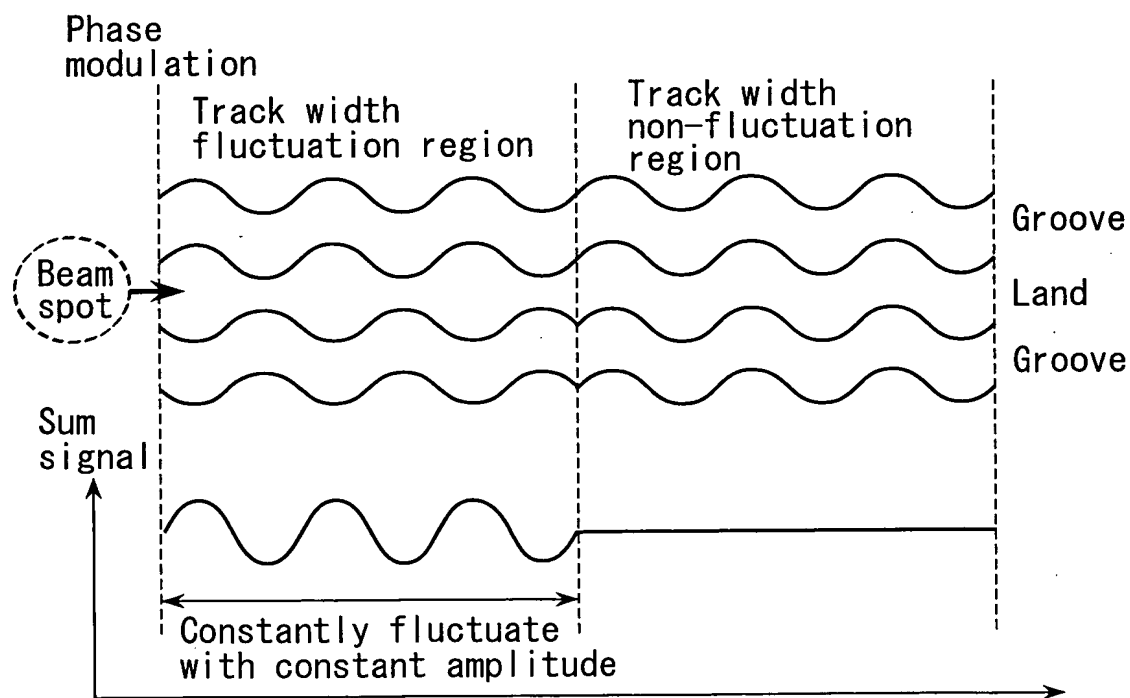


FIG. 26B



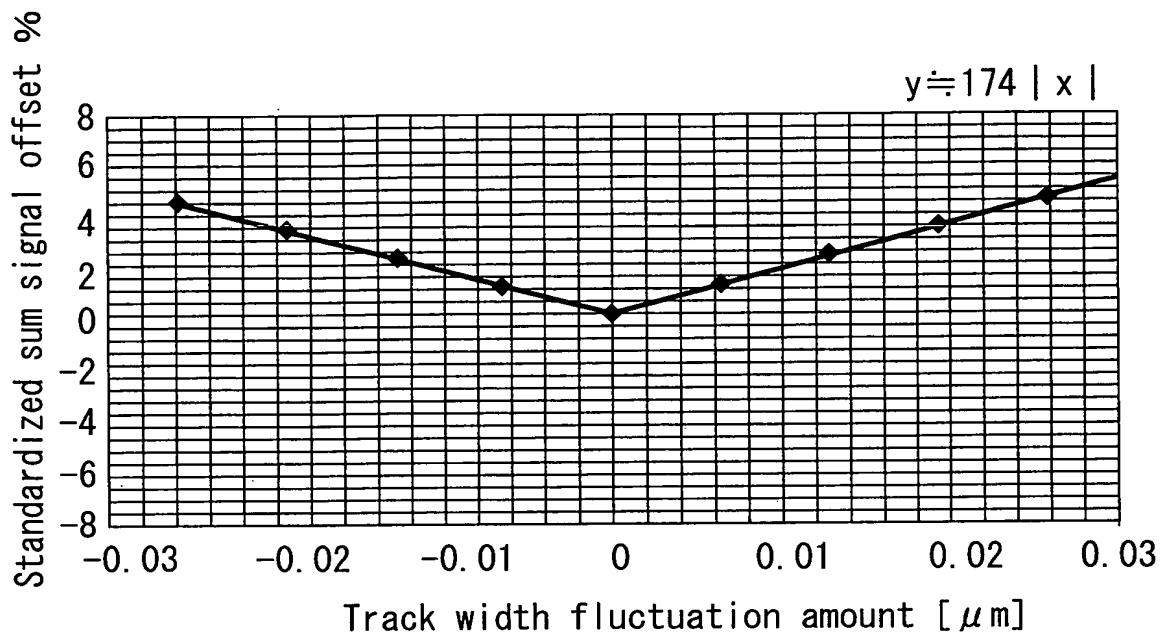


FIG. 27

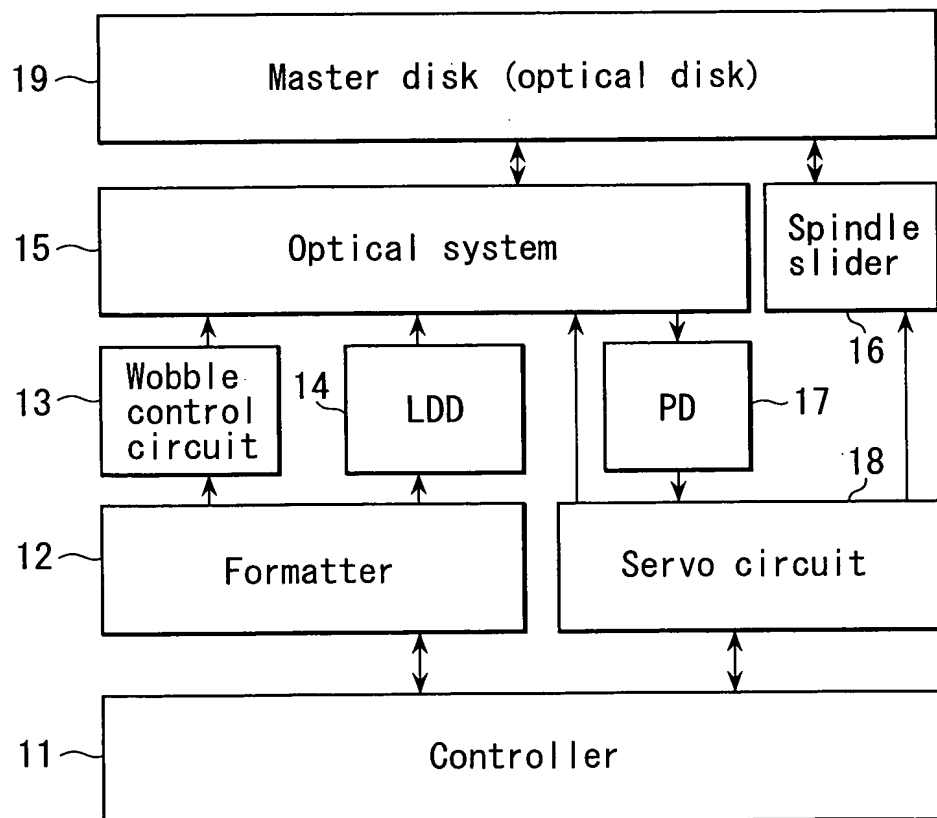


FIG. 28

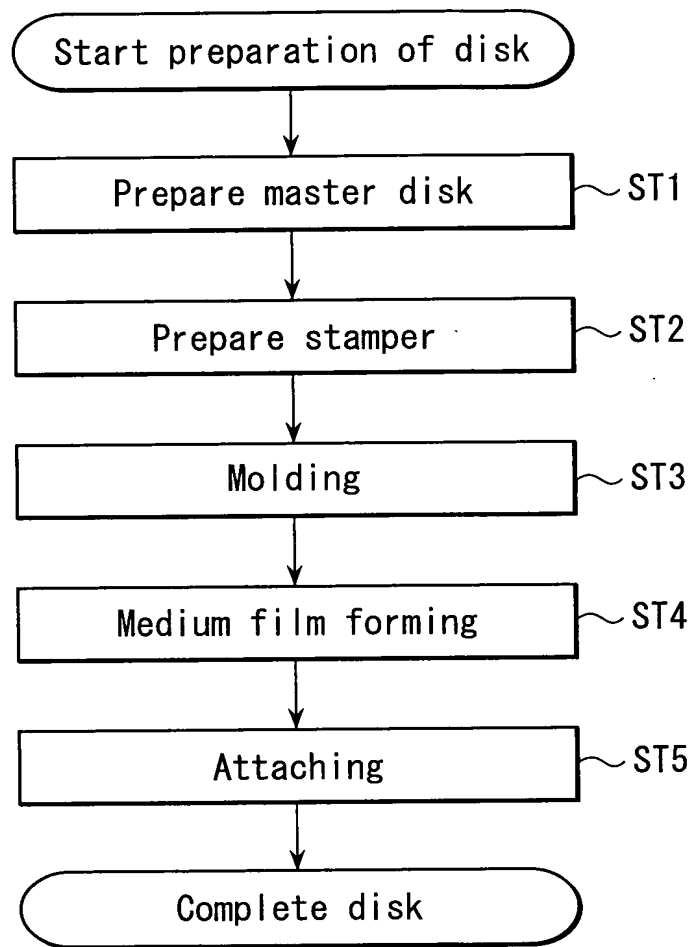


FIG. 29